	Search Text
1	node near5 overlap\$7 and timing near2 violat\$6
2	node near5 overlap\$7 and timing near2 (violat\$6 constraint)
3	node near5 overlap\$7 and timing near2 (violat\$6 constraint) and insert\$4 near4 (buffer driver repearter inverter)
4	node near5 overlap\$7 and timing near2 (violat\$6 constraint) and insert\$4 near4 (buffer driver repearter inverter) and node same path same ident\$7
5	node near5 overlap\$7 and timing near2 (violat\$6 constraint) and insert\$4 near4 (buffer driver repearter inverter) and node same path

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